

# Ahmet Cevdet Hayrullahoglu

Sabanci University

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## **EDUCATION**

### **Sabanci University**

**2007 – Present**

Faculty of Engineering and Natural Sciences (Electronics Engineering)

Sakip Sabanci 1/2 Scholarship awarded for high grades between 2009-2012.

Expected Graduation: June 2012

CGPA: 3.61/4.00

### **TED Ankara College (High School)**

**2003 – 2007**

CGPA: 91.27/100

## **INTERNSHIP**

### **AirTies**

**06/2011 – 08/2011**

Wrote C code in Ubuntu to record and decode coming data from server; established multicast client interface with universal datagram protocol; implemented transport stream parser which had capability of detecting video and audio codec types.

### **Karel**

**08/2011 – 09/2011**

Built processor-based hardware system on Altera Cyclone II FPGA by interconnecting hardware peripherals; coded FPGA in Verilog to perform receive, scan and verification interface of PS2 keyboard.

## **UNIVERSITY PROJECTS**

### **Cooperative Routing Architecture**

**Graduation Project 2011/2**

Implemented cross-layer multi-hop cooperative network architecture for wireless sensor networks; analyzed architecture in terms of energy costs considering mobility and multi source scenarios; modeled and simulated protocols/algorithms via network simulator-2 tool with project partner.

**DC Motor Position Control****Term Project 2011**

Controlled position of DC servo motor by using PIC24 board; checked servo and potentiometer values periodically; adjusted motor drive outputs to bring motor to identical position with potentiometer setting by using Assembly language.

**Panoramic Image****Term Project 2011**

Detected and matched key points in different images by applying Sift algorithm; stitched corresponding images by using Homographic transformation; created panoramic image.

**Model of a Bilinear Demosaicing****Term Project 2011**

Designed color filter array interpolation of Bayer pattern; implemented Verilog RTL model of image processing pattern on Xilinx Spartan FPGA.

**Electronic Ping-Pong Machine****Term Project 2010**

Designed electronic game of ping-pong that takes two players to play; modeled finite-state machine by using Verilog language; implemented on Xilinx Spartan FPGA.

**SKILLS**

**Programming Languages:** C/C++, HTML, Assembly, Verilog, Tcl

**Software and Tools:** Matlab, Mplab, Pspice, ModelSim, Xilinx ISE, Quartus 2, Network Simulator-2, Adobe Photoshop, Flash, Dreamweaver, Visual Studio 6.0, Eclipse

**Operating Systems:** Windows 95 / 98 / XP / VISTA / 7, Ubuntu

**Languages:** Turkish (Native), English (Advanced)

**SOCIAL ACTIVITIES & INTERESTS**

- Member, Sabanci IEEE Student Branch (2009 - Present)
- Member, Sabanci Computer Club (2008 - Present)
- President, Sabanci Economy and Management Club – Marketing Department (2010 - Present)
- Sun Day, Civic Involvement Project, Sabanci University Campus, Istanbul (May 13, 2008)
- Support to Elderly People, Civic Involvement Project, Istanbul (Spring 2008)
- Areas of Interest: Tennis, Swimming, Web Design

**REFERENCES**

Prof. Ozgur Gurbuz, Sabanci University, Istanbul, Turkey

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